

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L13	0	@ad<"19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and ((latch or (latch adj circuit)) with (delay with clock)) and 714/700.ccls.	USPAT	OR	OFF	2005/01/11 15:56
L12	1	@ad<"19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and ((latch or (latch adj circuit)) with (delay with clock)) and 370/503.ccls.	USPAT	OR	OFF	2005/01/11 15:56
L11	2	@ad<"19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and ((latch or (latch adj circuit)) with (delay with clock)) and 327/261.ccls.	USPAT	OR	OFF	2005/01/11 15:56
L10	0	@ad<"19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and ((latch or (latch adj circuit)) with (delay with clock)) and 327/231.ccls.	USPAT	OR	OFF	2005/01/11 15:56
L9	2	@ad<"19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and ((latch or (latch adj circuit)) with (delay with clock)) and 327/141.ccls.	USPAT	OR	OFF	2005/01/11 15:56
L8	3	@ad<"19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and ((latch or (latch adj circuit)) with (delay with clock)) and 375/354-357.ccls.	USPAT	OR	OFF	2005/01/11 15:56
S36	214	@ad<="19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and ((latch or (latch adj circuit)) with (delay with clock))	USPAT	OR	OFF	2005/01/11 15:55
L7	0	@ad<"19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and (jitter with reduc\$4) and (clock adj (generat\$3 or means)) and (delay adj time) and 714/700.ccls.	USPAT	OR	OFF	2005/01/11 15:48
L6	0	@ad<"19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and (jitter with reduc\$4) and (clock adj (generat\$3 or means)) and (delay adj time) and 327/261.ccls.	USPAT	OR	OFF	2005/01/11 15:48
L5	0	@ad<"19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and (jitter with reduc\$4) and (clock adj (generat\$3 or means)) and (delay adj time) and 327/231.ccls.	USPAT	OR	OFF	2005/01/11 15:47
L4	1	@ad<"19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and (jitter with reduc\$4) and (clock adj (generat\$3 or means)) and (delay adj time) and 327/141.ccls.	USPAT	OR	OFF	2005/01/11 15:47

L3	1	@ad<"19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and (jitter with reduc\$4) and (clock adj (generat\$3 or means)) and (delay adj time) and 370/503.ccls.	USPAT	OR	OFF	2005/01/11 15:46
L2	0	@ad<"19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and (jitter with reduc\$4) and (clock adj (generat\$3 or means)) and (delay adj time) and 375/354-357.ccls.	USPAT	OR	OFF	2005/01/11 15:45
S5	55	@ad<="19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and (jitter with reduc\$4) and (clock adj (generat\$3 or means)) and (delay adj time)	USPAT	OR	OFF	2005/01/11 15:40
S39	0	@ad<="19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A").ti and ((latch or (latch adj circuit)) with (delay with clock))	USPAT	OR	OFF	2004/03/31 17:48
S38	26	@ad<="19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and ((latch or (latch adj circuit)) with (delay with clock)) and (delay adj element)	USPAT	OR	OFF	2004/03/31 17:45
S37	214	@ad<="19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and ((latch or (latch adj circuit)) with (delay with clock)) and delay	USPAT	OR	OFF	2004/03/31 17:45
S35	1	chang-hank.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/08/27 17:15
S33	1	chang-wei-h.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/08/27 17:15
S34	1	yeh-edith.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/08/27 16:59
S32	23	chang-wei.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/08/27 16:59
S27	10	@ad<="19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A").ti. and "375"/\$.ccls.	USPAT	OR	OFF	2003/08/27 12:30
S31	44	@ad<="19980904" and yamada-yuji.in.	USPAT	OR	OFF	2003/08/27 11:36
S30	1	@ad<="19980904" and yamada-yuji.in. and (latch same delay)	USPAT	OR	OFF	2003/08/27 11:36

S29	1	@ad<="19980904" and yamada-yuji.in. and "375"/\$.ccls.	USPAT	OR	OFF	2003/08/27 11:34
S15	7	@ad<="19980904" and reich-werner.in.	USPAT	OR	OFF	2003/08/27 11:33
S28	2	@ad<="19980904" and (((digital adj to adj analog adj converter) or DAC or "D/A").ti. and (latch same delay same clock)	USPAT	OR	OFF	2003/08/26 17:43
S26	223	@ad<="19980904" and (((digital adj to adj analog adj converter) or DAC or "D/A").ti	USPAT	OR	OFF	2003/08/26 17:42
S23	0	@ad<="19980904" and (((digital adj to adj analog adj converter) or DAC or "D/A").ti. and ((latch or (latch adj circuit)) same (delay with clock))	USPAT	OR	OFF	2003/08/26 17:34
S25	4	@ad<="19980904" and (((digital adj to adj analog adj converter) or DAC or "D/A") same ((latch or (latch adj circuit)) with (delay with clock))) and "375"/\$.ccls.	USPAT	OR	OFF	2003/08/26 17:31
S24	30	@ad<="19980904" and (((digital adj to adj analog adj converter) or DAC or "D/A") and ((latch or (latch adj circuit)) with (delay with clock)) and "375"/\$.ccls.	USPAT	OR	OFF	2003/08/26 17:30
S22	0	@ad<="19980904" and (((digital adj to adj analog adj converter) or DAC or "D/A").ti. and ((latch or (latch adj circuit)) same (delay with clock)) and "375"/\$.ccls.	USPAT	OR	OFF	2003/08/26 17:29
S21	0	@ad<="19980904" and (((digital adj to adj analog adj converter) or DAC or "D/A").ti. and ((latch or (latch adj circuit)) same (delay with clock)) and "375"/\$.ccls.	USPAT	OR	OFF	2003/08/26 17:29
S19	48	@ad<="19980904" and (((digital adj to adj analog adj converter) or DAC or "D/A") and ((latch or (latch adj circuit)) same (delay with clock)) and "375"/\$.ccls.	USPAT	OR	OFF	2003/08/26 17:29
S20	0	@ad<="19980904" and (((digital adj to adj analog adj converter) or DAC or "D/A") and ((latch or (latch adj circuit)) same (delay with clock)) and 375/377.ccls.	USPAT	OR	OFF	2003/08/26 17:28
S18	137	@ad<="19980904" and (((digital adj to adj analog adj converter) or DAC or "D/A") and (latch or (latch adj circuit)) and (delay with clock) and "375"/\$.ccls.	USPAT	OR	OFF	2003/08/26 17:27
S17	1133	@ad<="19980904" and (((digital adj to adj analog adj converter) or DAC or "D/A") and (latch or (latch adj circuit)) and (delay with clock)	USPAT	OR	OFF	2003/08/26 17:25
S3	331	@ad<="19980904" and (((digital adj to adj analog adj converter) or DAC or "D/A") and (jitter with reduc\$4)	USPAT	OR	OFF	2003/08/26 16:06
S16	18	@ad<="19980904" and (delay adj element) and (latch adj circuit) and (clock adj generat\$3) and "375"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2003/08/26 12:06

S13	29	@ad<="19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and (jitter with reduc\$4) and (clock adj generat\$3) and "375"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2003/08/26 11:49
S14	8	@ad<="19980904" and pfeifer-heinrich.in.	USPAT	OR	OFF	2003/08/26 11:47
S1	0	@ad<="19980904" and (digital adj to adj analog adj converter) and jitter	USPAT	OR	OFF	2003/08/26 11:46
S4	29	@ad<="19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and (jitter with reduc\$4) and (clock adj generat\$3) and "375"/\$.ccls.	USPAT	OR	OFF	2003/02/24 10:39
S12	55	@ad<="19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and (jitter with reduc\$4) and (clock adj (generat\$3 or means)) and (delay adj time)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2003/02/24 10:38
S11	2	@ad<="19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and (jitter with reduc\$4) and 341/126.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2003/02/24 10:37
S10	2	@ad<="19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and (jitter with reduc\$4) and 341/126.ccls.	USPAT	OR	OFF	2003/02/24 10:36
S9	83	@ad<="19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and (jitter with reduc\$4) and "375"/\$.ccls.	USPAT	OR	OFF	2003/02/14 14:15
S8	0	yeh.xa. and "375"/\$.ccls.	USPAT	OR	OFF	2003/02/14 13:30
S7	65	yeh.xa.	USPAT	OR	OFF	2003/02/14 13:30
S6	0	yeh-edith.xa.	USPAT	OR	OFF	2003/02/14 13:30
S2	1748	@ad<="19980904" and ((digital adj to adj analog adj converter) or DAC or "D/A") and jitter	USPAT	OR	OFF	2003/02/13 15:56